



#16  
10/17/02  
Moore

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. .... 09/602,395  
Filing Date ..... June 22, 2000  
Inventor ..... John T. Moore  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Pham, T.  
Attorney Docket No. .... MI22-1384  
Title: Methods of Forming Oxide Regions Over Semiconductor Substrates

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 C.F.R. §1.56. Copies of the cited art are included. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed before the mailing of a first Office Action following the filing of a Request for Continued Examination. Therefore, no fee is believed to be required per 37 C.F.R. § 1.97(b)(4). However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated:

October 7, 2002

Attorney:

Jennifer J. Taylor  
Jennifer J. Taylor, Ph.D.  
Reg. No. 48,711

RECEIVED  
OCT 15 2002  
TECHNOLOGY CENTER 2800

EV182662160